


**Electronic Filing System (EFS) Data**  
**Electronic Patent Application Submission**  
**USPTO Use Only**

EFS ID: 65471  
Application ID: 10710692   
Title of Invention: High Voltage Circuits  
Implemented Using Low Voltage  
Transistors  
First Named Inventor: Sanish JACOB  
Domestic/Foreign Application: Domestic Application  
Filing Date: 2004-07-28  
Effective Receipt Date: 2004-07-28  
Submission Type: Utility Patent Filing  
Filing Type: new-utility  
Confirmation number: 4691  
Attorney Docket Number: TI-37611  
  
Total Fees Authorized: 2880.0  
Payment Category: Deposit Account  
Deposit Account Number: 200668  
Deposit Account Name: Narendra R. Thappeta  
RAM Payment Status: RAM has not been processed

Digital Certificate Holder: cn=Narendra R. Thappeta,ou=Registered Attorneys,ou=Patent and  
Trademark Office,ou=Department of Commerce,o=U.S. Government,c=US  
Certificate Message Digest: f81c288953b85d3165eb13feef065b8e8b55706d

PATENT APPLICATION SERIAL NO. \_\_\_\_\_

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE  
FEE RECORD SHEET

07/29/2004 HDEMESS1 00000037 200668 10710692

01 FC:1001	770.00 DA
02 FC:1201	774.00 DA
03 FC:1202	1296.00 DA

PTO-1556  
(5/87)